FIG. 1

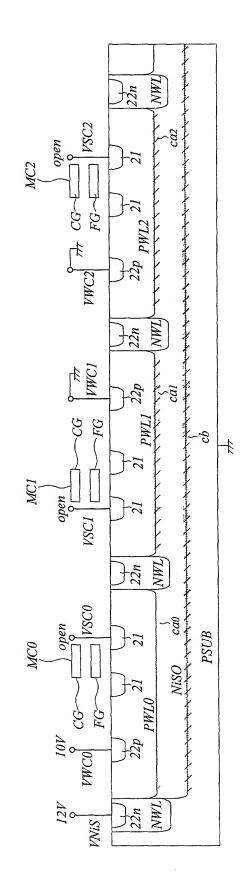
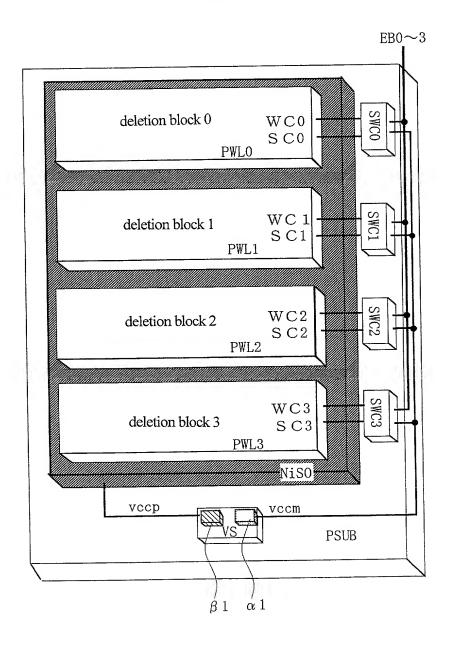
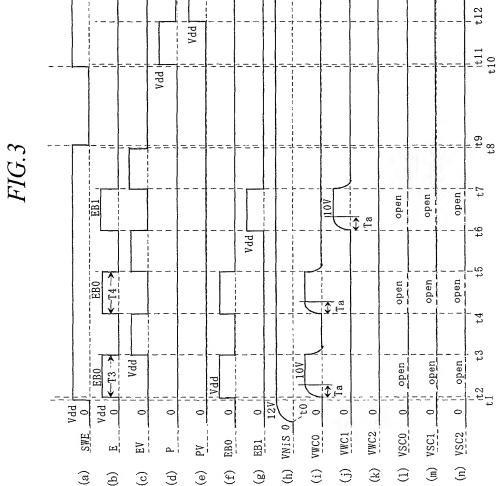


FIG.2





(J)

(e)

(a)

(p)

(၁

(P)

(g)

(K)

(5)

FIG.4

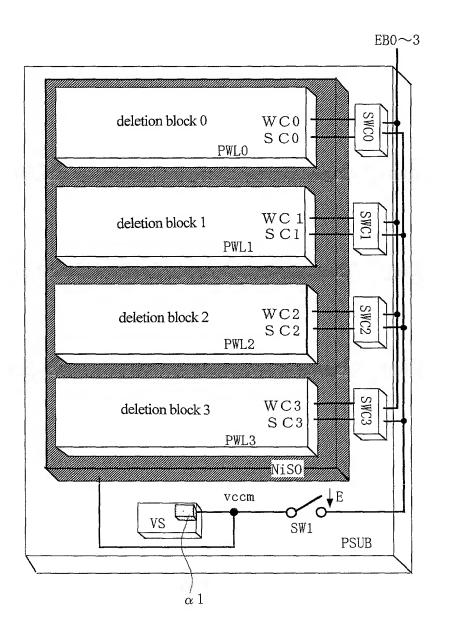


FIG. 5

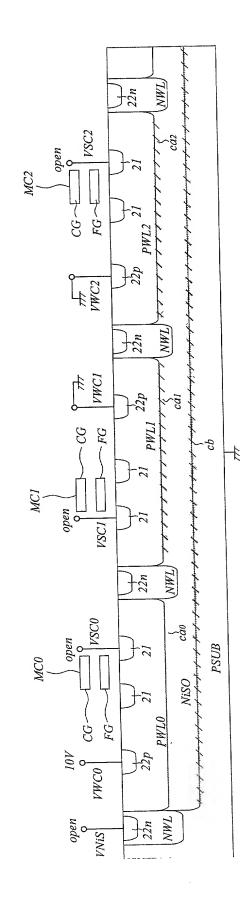
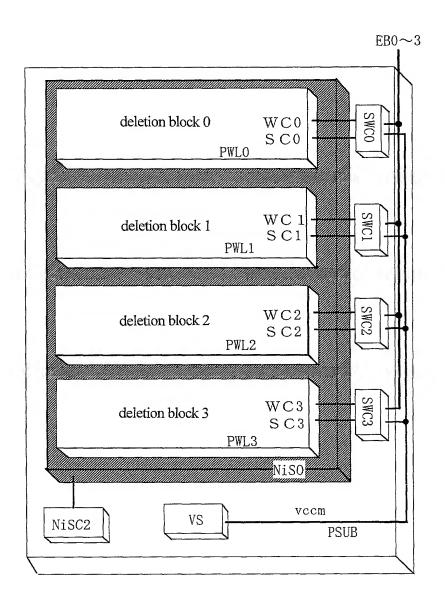


FIG.6





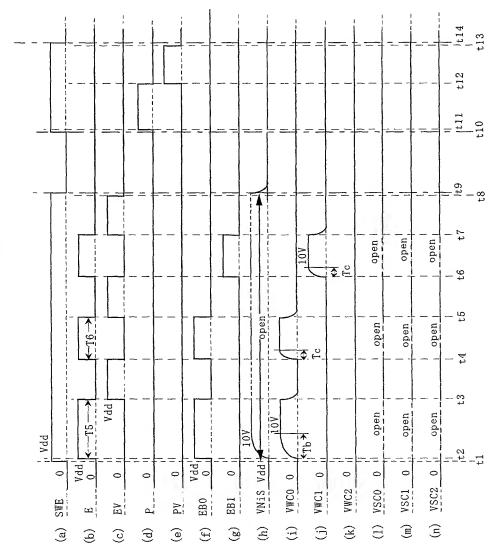


FIG.8

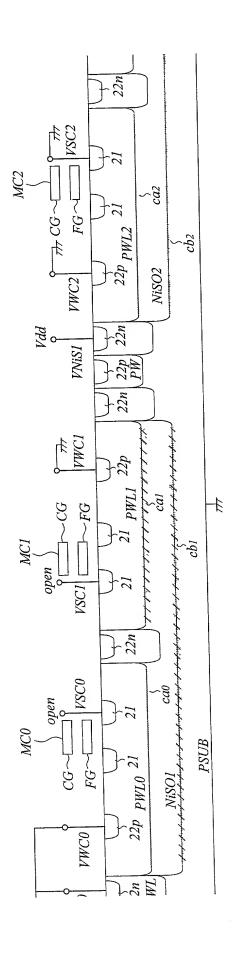
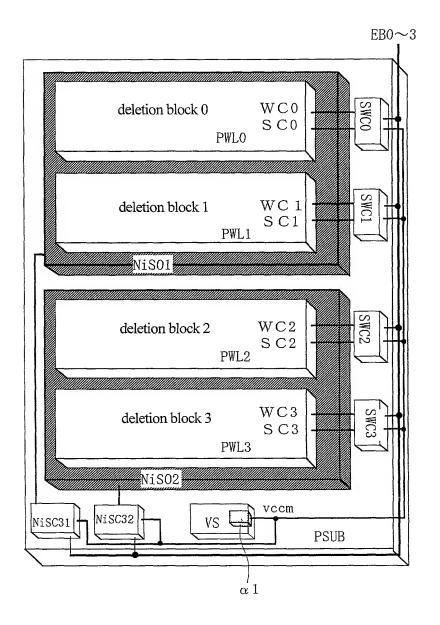


FIG.9



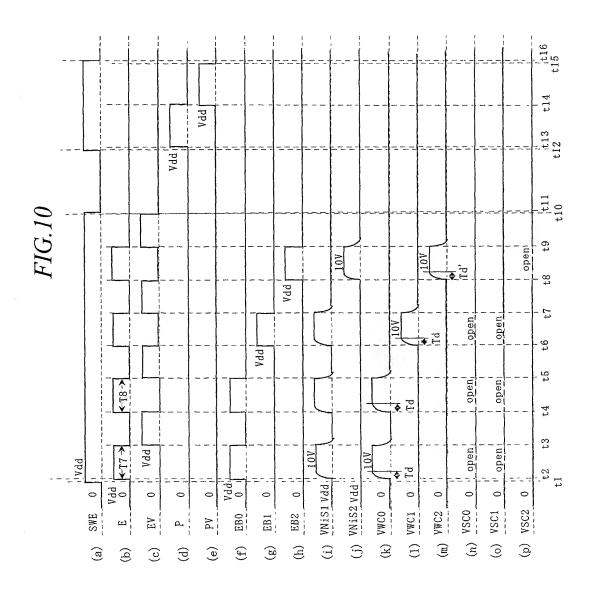


FIG.11

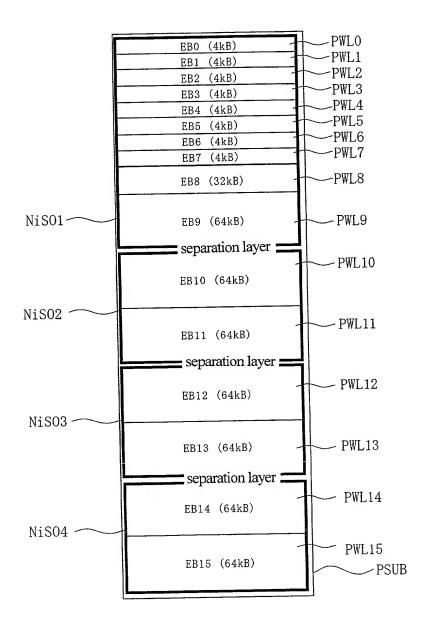


FIG.12

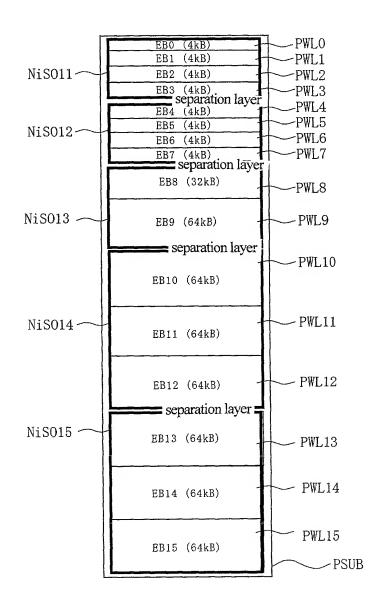
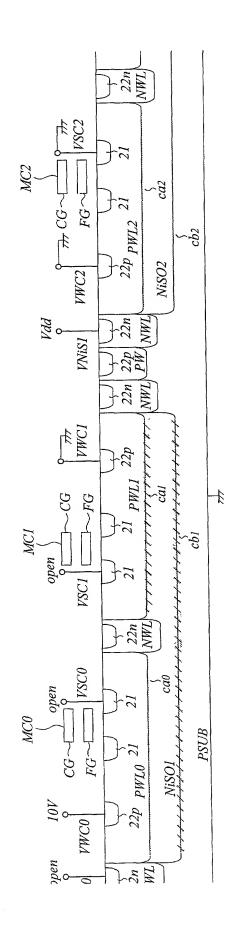
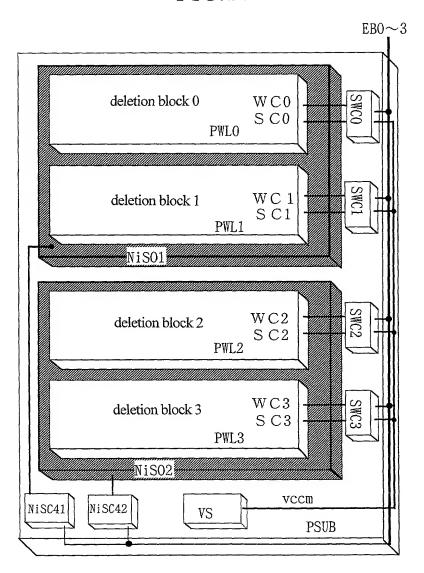


FIG 13



TO STATE THE TAXABLE STATE OF THE PROPERTY OF

FIG.14



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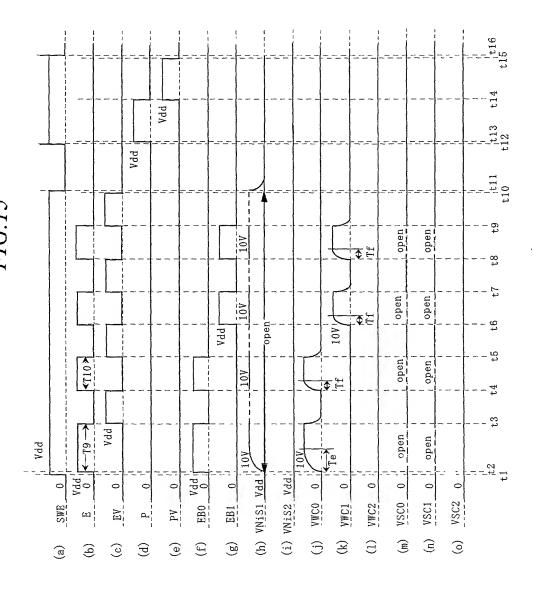


FIG.16A

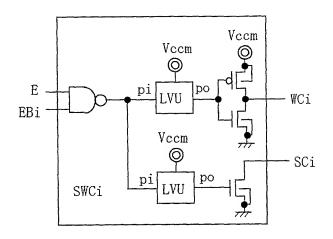


FIG.16B

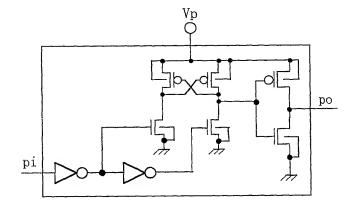


FIG.17

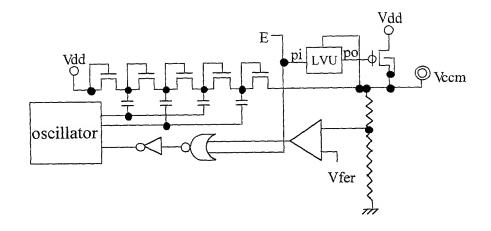


FIG.18

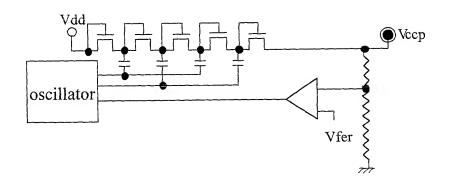


FIG.19

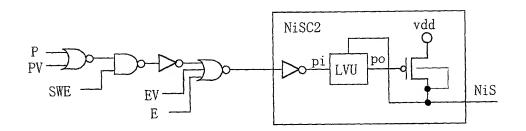


FIG.20

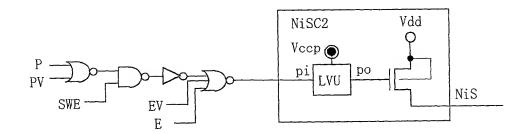


FIG.21

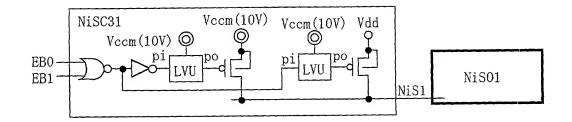
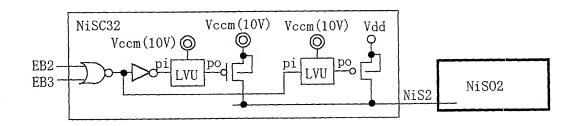


FIG.22



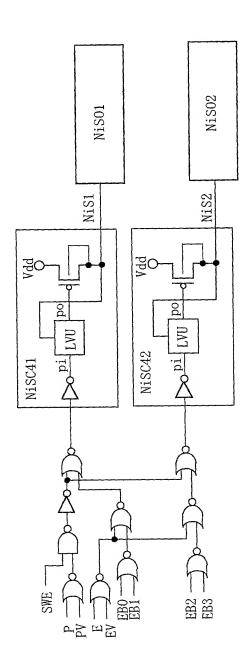


FIG.24

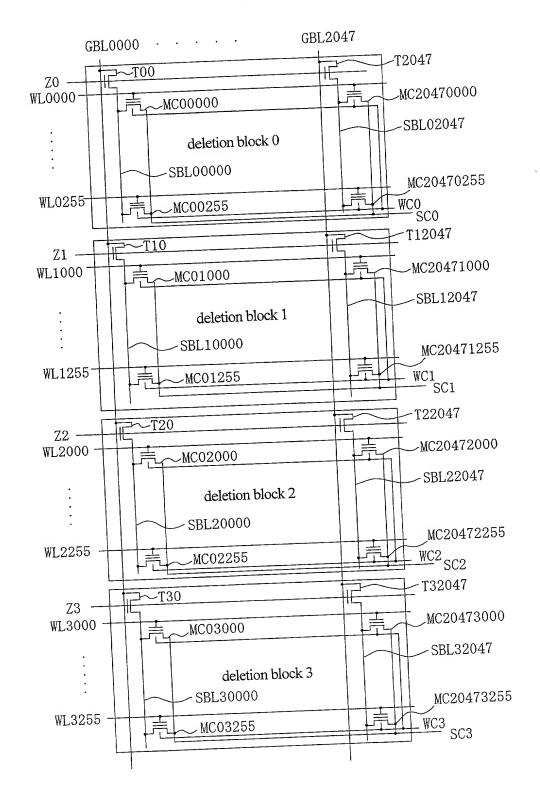


FIG.25

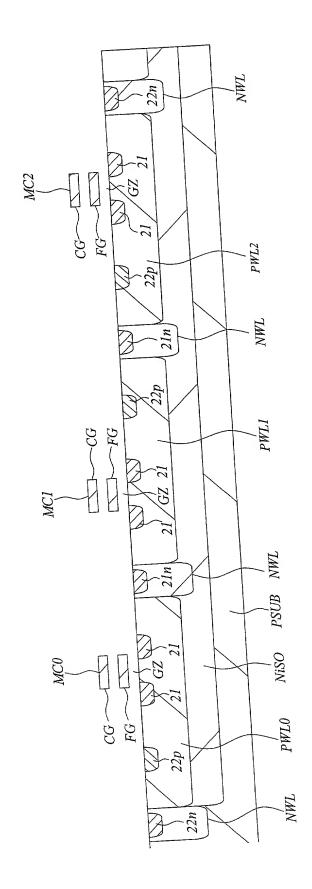


FIG.26A

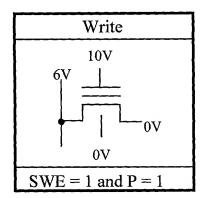


FIG.26B

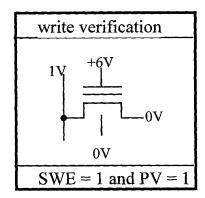


FIG.26C

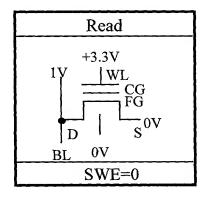


FIG.26D

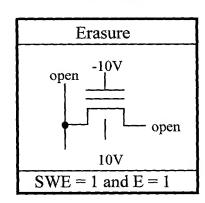


FIG.26E

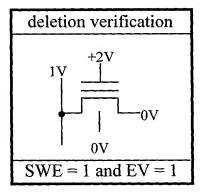


FIG.27A

FIG.27B

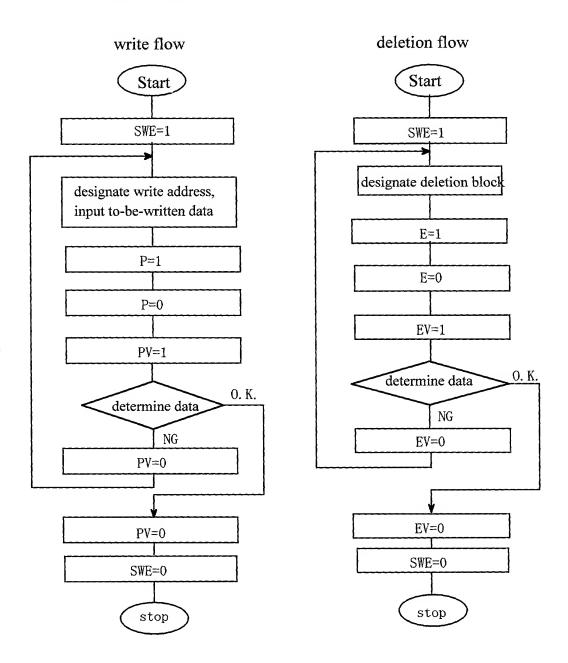
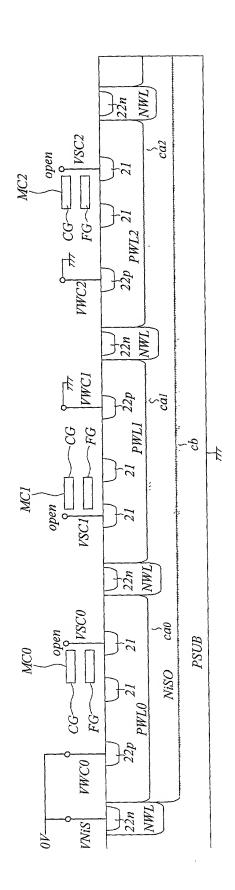


FIG.28



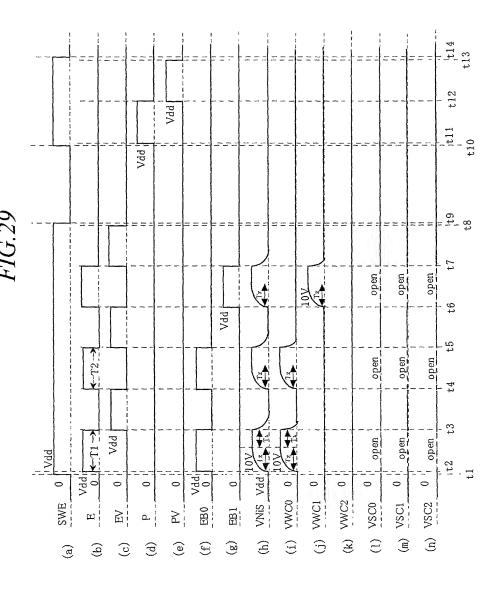


FIG.30

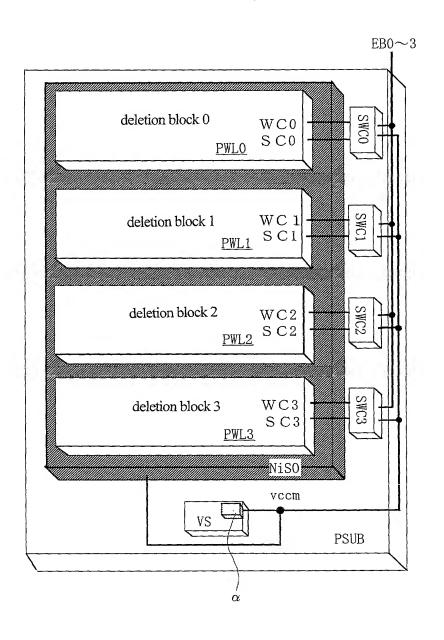
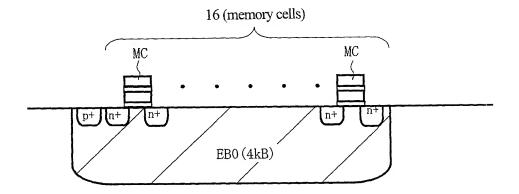


FIG.31



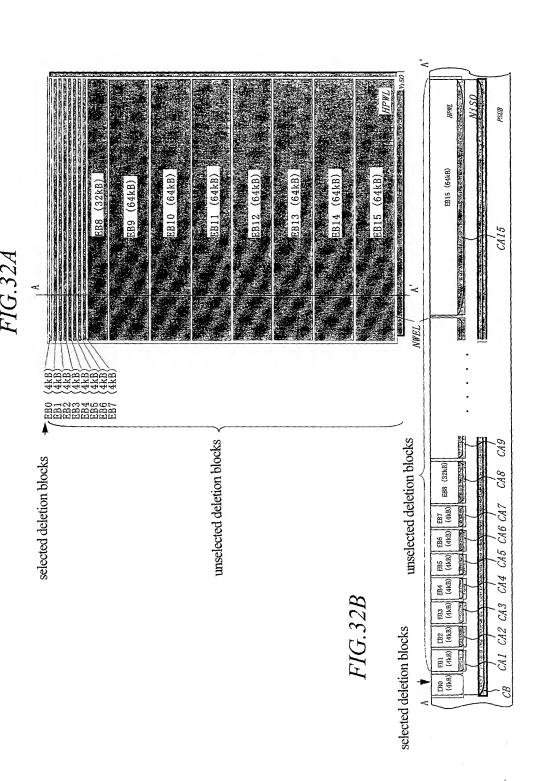


FIG.33

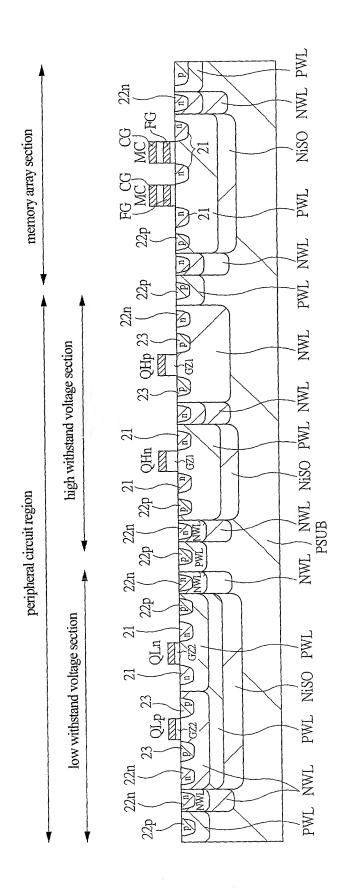


FIG.34

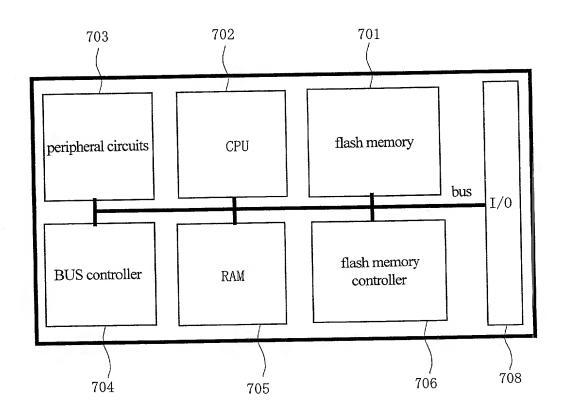


FIG.35

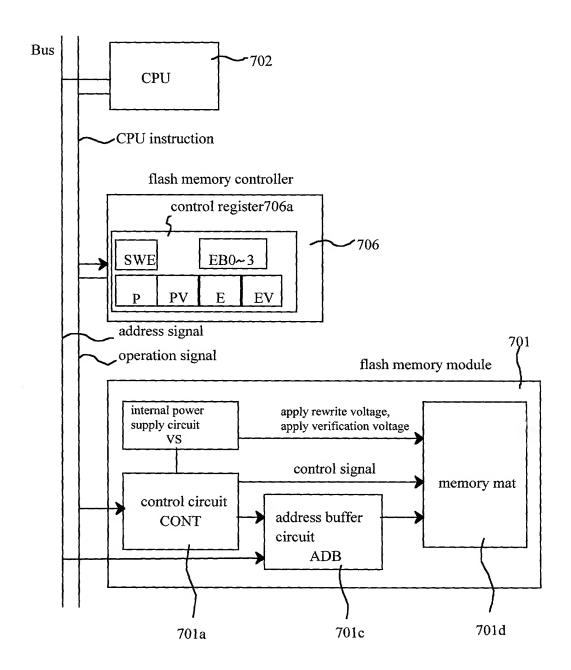
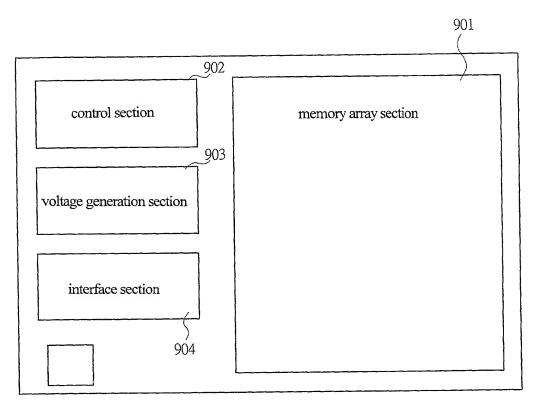


FIG.36



detection circuit

FIG.37

